

ABSTRACT OF THE DISCLOSURE

A sensor integrated on a single semiconductor substrate comprises a sensor block including a pixel unit including a plurality of pixels each including a light-receiving element and scanning units for selecting a pixel of the pixel unit, and a signal processing block for processing a signal output from the sensor block. To reduce noise and power consumption, the power supply voltage or the amplitude 5 or high level of a clock signal used in the sensor block is higher than the power supply voltage of the signal processing block.

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